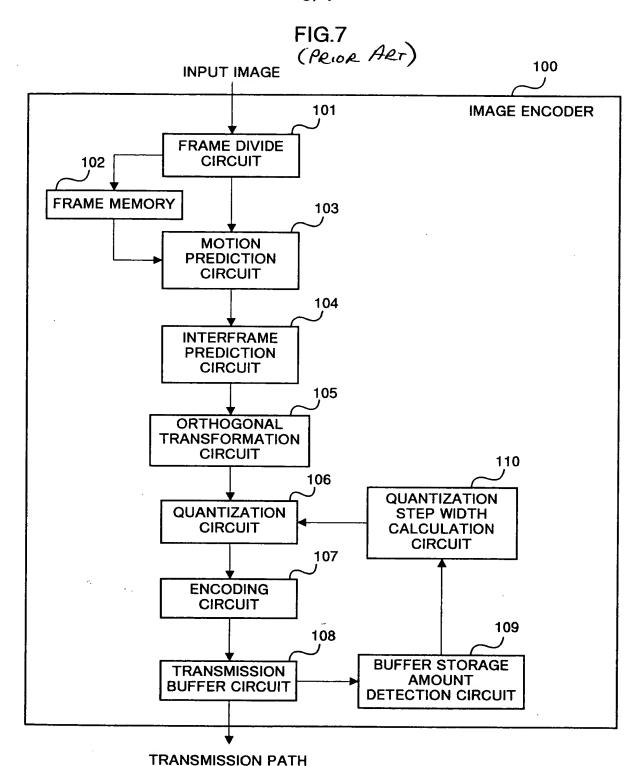
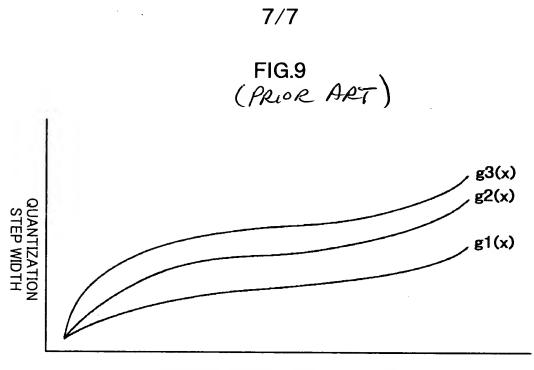
5/7



6/7 FIG.8 (PRIOR ART) **INPUT IMAGE** 200 201 **IMAGE ENCODER** FRAME DEVIDE 202 **CIRCUIT** FRAME MEMORY 203 211 **MOTION MOTION-PREDICTION VECTOR-VALUE CIRCUIT MEMORY** 204 212 **INTERFRAME PREDICTION BLOCK GROUPING CIRCUIT SECTION** 205 213 ORTHOGONAL **FUNCTION** TRANSFORMATION **SELECTOR CIRCUIT** 214 206 **QUANTIZATION STEP QUANTIZATION** WIDTH CALCULATION **CIRCUIT CIRCUIT** 207 **ENCODING CIRCUIT** 209 208 **BUFFER STORAGE TRANSMISSION AMOUNT BUFFER CIRCUIT DETECTION CIRCUIT** TRANSMISSION PATH



STORAGE AMOUNT (X) IN BUFFER